

This listing of claims will replace all prior versions, and listings, of claims in the application:

Amendments

In the Claims:

1. (Currently Amended) A method of indicating a status affected by the performance of an ALU mathematical operation, comprising:

~~executing~~ fetching an ALU mathematical operation instruction ~~on a set of source operands;~~

determining that the ALU mathematical operation instruction corresponds to an ALU mathematical operation instruction ~~with~~ specifying the performance of a carry operation;

~~executing an ALU mathematical operation producing a result based on the a set of~~ variable width source operands in accordance with the ALU mathematical operation instruction to produce a result; and

~~indicating setting~~ a status of the ALU mathematical operation is flag based on the result.

2. (Currently Amended) The method according to claim 1, wherein the step of ~~setting~~ indicating the status of the ALU mathematical operation flag includes the step of determining that the result is a non-zero value.

3. (Currently Amended) The method according to claim 2, wherein the step of ~~setting~~ indicating the status of the ALU mathematical operation flag includes the step of clearing the a status flag by writing a value of zero to the status flag.

4. (Currently Amended) The method according to claim 3, wherein the step of setting indicating the status of the ALU mathematical operation flag includes the step of maintaining the value of zero in the status flag until an ALU mathematical operation instruction ~~without~~ that excludes specifying the performance of an operation including a carry is determined executed.
5. (Currently Amended) The method according to claim 1, wherein the step of setting indicating the status of the ALU mathematical operation flag includes the step of determining that the result is a zero value.
6. (Currently Amended) The method according to claim 5, wherein the step of setting indicating the status of the ALU mathematical operation flag includes the step of maintaining the value in the status flag.
7. (Currently Amended) A processor for indicating a status affected by the performance of an ALU mathematical operation, comprising:
an instruction decode unit operable to:
fetch an ALU mathematical operation instruction ~~on a set of source operands; and~~
determine that the ALU mathematical operation instruction corresponds to an ALU mathematical operation instruction with specifying the performance of a carry operation;
an ALU, coupled to the instruction decode unit, operable to:
execute an ALU mathematical operation ~~produce a result based on the a~~ set of
variable width source operands in accordance with the ALU mathematical operation instruction to produce a result; and

indicate setting a status of the ALU mathematical operation is flag based on the result.

8. (Original) The processor according to claim 7, further comprising the ALU operable to determine that the result is a non-zero value.

9. (Currently Amended) The processor according to claim 8, further comprising the ALU operable to clear ~~the~~ a status flag by writing a value of zero to the status flag.

10. (Currently Amended) The processor according to claim 9, further comprising the ALU operable to maintain the value of zero in the status flag until an ALU mathematical operation instruction ~~without~~ that excludes specifying the performance of an operation including a carry is determined executed.

11. (Original) The processor according to claim 7, further comprising the ALU operable to determine that the result is a zero value.

12. (Original) The processor according to claim 11, further comprising the ALU operable to maintain the value of the status flag.